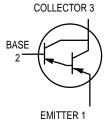
Darlington Transistors PNP Silicon



MAXIMUM RATINGS

Rating	Symbol	MPSA62	MPSA63 MPSA64	Unit
Collector-Emitter Voltage	VCES	-20	-30	Vdc
Collector-Base Voltage	VCBO	-20	-30	Vdc
Emitter-Base Voltage	VEBO	-10		Vdc
Collector Current — Continuous	IC	-500		mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0		mW mW/°C
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	1.5 12		Watts mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	w w.55 it ton±1:50 om		°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta}JA$	200	°C/W
Thermal Resistance, Junction to Case	$R_{\theta}JC$	83.3	°C/W

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I _C = –100 μAdc, V _{BE} = 0)	MPSA62 MPSA63, MPSA64	V(BR)CES	-20 -30		Vdc
Collector Cutoff Current ($V_{CB} = -15 \text{ Vdc}$, $I_{E} = 0$) ($V_{CB} = -30 \text{ Vdc}$, $I_{E} = 0$)	MPSA62 MPSA63, MPSA64	ICBO		-100 -100	nAdc
Emitter Cutoff Current (V _{EB} = -10 Vdc, I _C = 0)		I _{EBO}	_	-100	nAdc

Preferred devices are Motorola recommended choices for future use and best overall value.

MPSA62 thru MPSA64 *

MPSA55, MPSA56

For Specifications, See MPSA05, MPSA06 Data

*Motorola Preferred Device



CASE 29-04, STYLE 1 TO-92 (TO-226AA)



MPSA62 thru MPSA64

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Max	Unit
ON CHARACTERISTICS(1)			•		•
DC Current Gain (I _C = -10 mAdc, V _{CE} = -5.0 Vdc)	MPSA63 MPSA64 MPSA62	hFE	5,000 10,000 20,000	_ _ _ _	_
$(I_C = -100 \text{ mAdc}, V_{CE} = -5.0 \text{ Vdc})$	MPSA63 MPSA64		10,000 20,000	_ _	
Collector-Emitter Saturation Voltage (I _C = -10 mAdc, I _B = -0.01 mAdc) (I _C = -100 mAdc, I _B = -0.1 mAdc)	MPSA62 MPSA63, MPSA64	VCE(sat)		-1.0 -1.5	Vdc
Base-Emitter On Voltage ($I_C = -10$ mAdc, $V_{CE} = -5.0$ Vdc) ($I_C = -100$ mAdc, $V_{CE} = -5.0$ Vdc)	MPSA62 MPSA63, MPSA64	V _{BE} (on)	_ _	-1.4 -2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS	_				
Current-Gain — Bandwidth Product ⁽²⁾ (I _C = -100 mAdc, V _{CE} = -5.0 Vdc, f = 100 MHz)	MPSA63, MPSA64	fT	125	_	MHz

^{1.} Pulse Test: Pulse Width $\leq~300~\mu\text{s};$ Duty Cycle $\leq~2.0\%.$

^{2.} $f_T = |h_{fe}| \cdot f_{test}$.

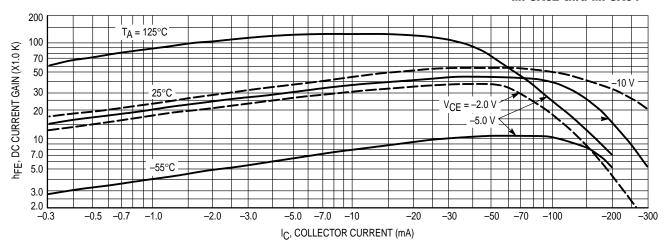


Figure 1. DC Current Gain

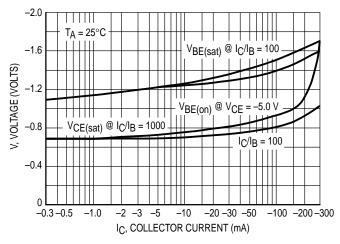


Figure 2. "On" Voltage

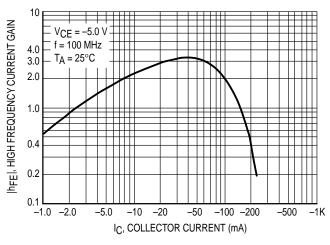


Figure 4. High Frequency Current Gain

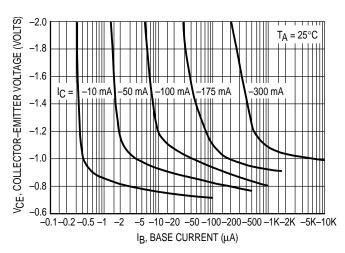


Figure 3. Collector Saturation Region

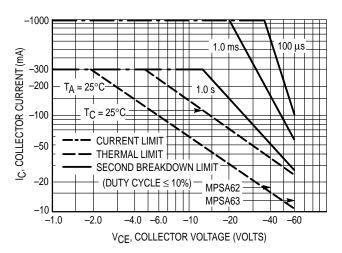
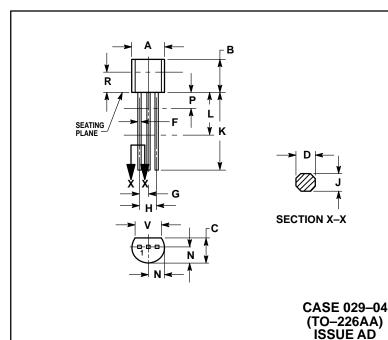


Figure 5. Active Region, Safe Operating Area

PACKAGE DIMENSIONS



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
 CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- DIMENSION F APPLIES BETWEEN P AND L. DIMENSION F APPLIES BETWEEN F AIND L.
 DIMENSION D AND J APPLY BETWEEN L AND K
 MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIM	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
V	0.135		3 43	

STYLE 1:

PIN 1. EMITTER BASE 3. COLLECTOR

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